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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/871,463	05/31/2001	Charles R. Spinner III	01-P-002 (STM101-00013)	9805
30425	7590	09/06/2005	EXAMINER	
STMICROELECTRONICS, INC. MAIL STATION 2346 1310 ELECTRONICS DRIVE CARROLLTON, TX 75006			WARREN, MATTHEW E	
			ART UNIT	PAPER NUMBER
			2815	

DATE MAILED: 09/06/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

# Office Action Summary

Application No.

09/871,463

Applicant(s)

SPINNER ET AL.

Examiner

Matthew E. Warren

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

## Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

- 1) ☒ Responsive to communication(s) filed on 20 June 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## Disposition of Claims

- 4) ☒ Claim(s) 8-20 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 8-20 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

## Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
  - ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

## Attachment(s)

- ☐ Notice of References Cited (PTO-892)
- ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_
- ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- ☐ Notice of Informal Patent Application (PTO-152)
- ☐ Other: \_\_\_\_\_

### DETAILED ACTION

This Office Action is in response to the RCE and Amendment filed on June 20, 2005.

#### ***Claim Rejections - 35 USC § 102***

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 8 and 10-14 are rejected under 35 U.S.C. 102(b) as being anticipated by Zhao et al. (US 6,060,787).

In re claim 8, Zhao et al. shows (fig. 3A) a portion of an integrated circuit comprising: a dielectric layer (302) over a substrate, a conformal tungsten layer (306) over the dielectric layer, within and filling openings within the dielectric layer. A protective barrier (307) of tungsten is formed over the tungsten layer and within the openings. The barrier layer comprises a material for which removal by chemical mechanical polishing is primarily mechanical because the tungsten barrier layer (307) resists attack by the polishing slurry (col. 6, lines 3-19). Although Zhao does not specifically call the protective tungsten layer (307) a barrier layer, however tungsten is well known to provide the barrier function according to the teachings of Horak et al. (col. 5, lines 34-52).

In re claim 10, Zhao et al. shows (fig. 3A) that the portions of the tungsten layer within the openings are thicker than the portions of the tungsten layer over the dielectric layer.

In re claims 11 and 12, Zhao et al. shows (fig. 3A) that the protective barrier layer (307) overlies the entire tungsten layer (306). Zhao et al. also shows (fig. 3B) that the protective barrier layer overlies portions of the tungsten layer within the openings but not portions of the tungsten layer over the dielectric layer.

In re claims 13 and 14, Zhao et al. discloses (col. 5, lines 20-24 and col. 5, lines 50-55) that the tungsten layer has thickness between 4500 and 8000 Angstroms and the protective barrier layer has a thickness between 100 and 800 Angstroms.

Claims 8-12, 14, 16-19 are rejected under 35 U.S.C. 102(b) as being anticipated by Suzuki et al. (US 6,054,383)

In re claim 8, Suzuki et al. shows (figs. 2B-2C) a portion of an integrated circuit comprising: a dielectric layer (4) over a substrate (1), a conformal tungsten layer (8) over the dielectric layer, within and filling openings within the dielectric layer. A protective barrier (10a) of TiN is formed over the tungsten layer and within the openings. The barrier layer comprises a material for which removal by chemical mechanical polishing is primarily mechanical (col. 6, lines 59-65).

In re claim 10, Suzuki et al. shows (fig. 2B) that the portions of the tungsten layer within the openings are thicker than the portions of the tungsten layer over the dielectric layer because the portion in the hole consists of two layers of folded tungsten.

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In re claims 11 and 12, Suzuki et al. shows (fig. 2B) that the protective barrier layer (10) overlies the entire tungsten layer (8). Suzuki et al. also shows in another embodiment (fig. 4D) that the protective barrier (11c) layer overlies portions of the tungsten layer (9c) within the openings but not portions of the tungsten layer over the dielectric layer.

In re claim 14 Suzuki et al. discloses (col. 6, lines 1-2) that the protective barrier layer has a thickness of 0.05 microns (500 Angstroms), which is between 100 and 800 Angstroms.

In re claim 16, Suzuki et al. shows (fig. 2C) a portion of an integrated circuit structure comprising: a dielectric layer (4) having an opening, tungsten (8a) within the opening, and a portion of a protective barrier layer (10a) over a central region of the tungsten and within the opening, but not over peripheral regions of the tungsten. The protective barrier layer has the property of a material for which removal of chemical mechanical polishing is primarily mechanical (col. 6, lines 59-65) .

In re claim 17, Suzuki et al. shows (fig. 2C) that an upper surface of the tungsten is exposed around a portion of the protective barrier layer.

In re claim 18, Suzuki et al. discloses (col. 5, lines 62-67) that the protective barrier layer is titanium or titanium nitride.

In re claim 19, Suzuki et al. shows (fig. 2C) that the tungsten and the portion of the protective barrier layer form an upper surface, which is planar with an upper surface of the dielectric layer.

***Claim Rejections - 35 USC § 103***

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 9 is rejected under 35 U.S.C. 103(a) as being unpatentable over Zhao et al. (US 6,060,787) as applied to claim 8 above, and further in view of Horak et al. (US 6,436,814 B1).

In re claim 9, Zhao et al. discloses that the protective barrier is a tungsten film but does not explicitly show that the barrier film is a titanium or titanium nitride. Horak et al. discloses (col. 5, lines 34-52) that titanium or titanium nitride is a suitable material for a barrier layer and is interchangeable with a tungsten layer. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the tungsten barrier layer of Zhao by substituting it with titanium because Horak teaches that titanium is a suitable barrier layer for a tungsten plug.

Claim 15 is rejected under 35 U.S.C. 103(a) as being unpatentable over Zhao et al. (US 6,060,787) as applied to claim 8 above, and further in view of Van Buskirk et al. (US 6,346,741 B1).

Zhao et al. shows all of the elements of the claims except the opening in the dielectric being sized to form a capacitive electrode from the tungsten within the opening. Van Buskirk et al. shows. (fig. 1H) shows a capacitor device comprising a

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tungsten electrode contact (18) and a tungsten top electrode (44) formed in dielectric layer (18 and 35) openings. Therefore it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the tungsten interconnect of Zhao by incorporating that interconnect as a capacitor electrode because Van Buskirk teaches that tungsten interconnects suitably function as capacitor electrodes.

Claims 13, 15, and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Suzuki et al. (US 6,054,383) as applied to claims 8 and 16 above, and further in view of Van Buskirk et al. (US 6,346,741 B1).

In re claim 13, Suzuki et al. shows all of the elements of the claims except the tungsten layer having a thickness between 4500 and 8000 Angstroms. Although, Suzuki states that the thickness of the tungsten layer may be  $1/5$  to  $2/5$  the diameter of the contact hole to provide a satisfactory contact (col. 5, lines 47-54), Suzuki does not disclose the specific diameter of the hole. However, it would have been obvious to one of ordinary skill in the art to make the of the tungsten layer within the desired range, to provide satisfactory contact action. It has been held that where the general conditions of a claim are disclosed in the prior art, discovering the optimum or workable ranges involves only routine skill in the art. *In re Aller*, 105 USPQ 233.

In re claims 15 and 20, Suzuki et al. shows all of the elements of the claims except the opening in the dielectric being sized to form a capacitive electrode from the tungsten within the opening. Van Buskirk et al. shows. (fig. 1H) shows a capacitor device comprising a tungsten electrode contact (18) and a tungsten top electrode (44)

formed in dielectric layer (18 and 35) openings. Therefore it would have been obvious to one of ordinary skill in the art at the time the invention was made to modify the tungsten interconnect of Suzuki by incorporating that interconnect as a capacitor electrode because Van Buskirk teaches that tungsten interconnects suitably function as capacitor electrodes.

### ***Response to Arguments***

Applicant's arguments filed with respect to claims 8-20 have been fully considered but they are not persuasive. The applicant primarily asserts that the prior art references of Zhao et al. and Suzuki et al. do not show the amended limitation of the conformal tungsten layer filling openings within the dielectric layer. The examiner believes that the prior art references show all of the elements of the claims. As seen in figure 3A of Zhao, the conformal tungsten layer (306) fills the opening of the dielectric layer (302). As seen in figure 2D, the conformal tungsten layer (8) fills openings within the dielectric (4). Thus the prior art references show all of the elements of the claims, and the rejections above are still proper.

### ***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Matthew E. Warren whose telephone number is (571) 272-1737. The examiner can normally be reached on Mon-Thur and alternating Fri 9:00-5:00pm.



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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MEW  
*Mew*  
September 1, 2005

  
TOM THOMAS  
SUPERVISORY PATENT EXAMINER